

ABSTRACT OF THE DISCLOSURE

A digitally configurable multiplexer/de-multiplexer including several multiplexers, a switch matrix, and configuration logic. Each multiplexer receives multiple address signals, selects from among multiple first data signals and couples a selected first data signal to a corresponding one of several multiplexed signals. The switch matrix has a first interface coupled to the multiplexed signals and a second interface coupled to multiple second data signals. The configuration logic receives selection signals and controls the switch matrix to couple selected ones of the multiplexed signals to the second interface.